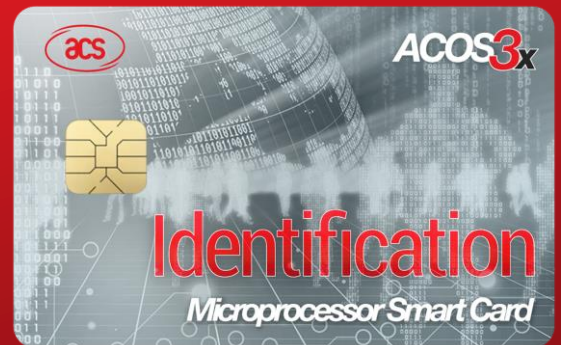




**Advanced Card Systems Ltd.**  
Card & Reader Technologies

# ACOS3X (Contact)



Functional Specifications V3.25



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## 1.0. Introduction

The purpose of this document is to describe in detail the features and functions of the ACOS3X, a versatile smart card operating system developed by Advanced Card Systems Ltd.

### 1.1. History of Modification for ACOS3X

Date	Changes
March 2009	ACOS3x revision 1.15 <ul style="list-style-type: none"> <li>• New product options with user storage capacity of 32, 72, and 256 Kbyte</li> <li>• Allows for 128 files to be stored</li> <li>• Uses FIPS 140-2 compliant Hardware Random Number Generator</li> <li>• Uses hardware based 3DES encryption engine</li> <li>• Issuing START SESSION clears authentication state for enhanced security</li> </ul>
December 2013	ACOS3x revision 1.22 <ul style="list-style-type: none"> <li>• Updated product on new IC platform. All functionality remains the same.</li> </ul>
February 2016	ACOS3X revision 1.30 <ul style="list-style-type: none"> <li>• Updated product on new IC platform, with increased maximum baud rate from 223.2kbps to 625 kbps</li> <li>• Allows for 64 files to be stored</li> <li>• Allows maximum 65535 bytes per file</li> </ul>

**Table 1:** History of Modifications for ACOS3X



## 2.0. Technical Specifications

The following are the features and technical properties of the ACOS3X card:

### 2.1. Electrical

- Operating Voltage: 2.1V to 5.5V
- Maximum Supply Current: < 10 mA
- ESD Protection: ≤ 5 KV

### 2.2. Environmental

- Operating Temperature: -25 °C to 85 °C
- Storage Temperature: -40 °C to 100 °C

### 2.3. Communication Protocol

- T=0 with baud up to 625 kbps

### 2.4. Memory

- Capacity: 256 KB
- EEPROM Endurance: 500,000 erase/write cycles
- Data Retention: 30 years

### 2.5. Cryptographic Capabilities

- DES/3DES: 56/112 bits
- AES: 128/192 bits

### 2.6. Random Number Generation

- FIPS 140-2 compliant hardware-based RNG

### 2.7. File Security

- Five secret codes + issuer code
- PIN code
- Key pair for mutual authentication
- Session key based on random numbers
- Secure Messaging function for confidential and authenticated data transfers
- Support for highly secured e-Purse for payment applications

### 2.8. Compliance to Standards

- Compliance to ISO 7816 Parts 1, 2, and 3



## 2.9. Answer to Reset (ATR)

After a hardware reset (e.g., power up), the card transmits an Answer-To-Reset (ATR) in compliance with the standard ISO 7816, part 3. ACOS3 supports the protocol type T=0. The protocol type selection function is not implemented.

The direct convention is used for the coding of the bits in the communication with the card, i.e., logic level ONE corresponds to the Z state of the I/O line.

Fourteen bytes of data are transmitted in the historical bytes as described below.

The following data are transmitted in the ATR:

TS	T0	TA <sub>1</sub>	TB <sub>1</sub>	TD <sub>1</sub>	14 Historical Bytes
3Bh	BEh	11h	00h	00h	

For more information, refer to **Section 3.2**.

### 3.0. Card Management

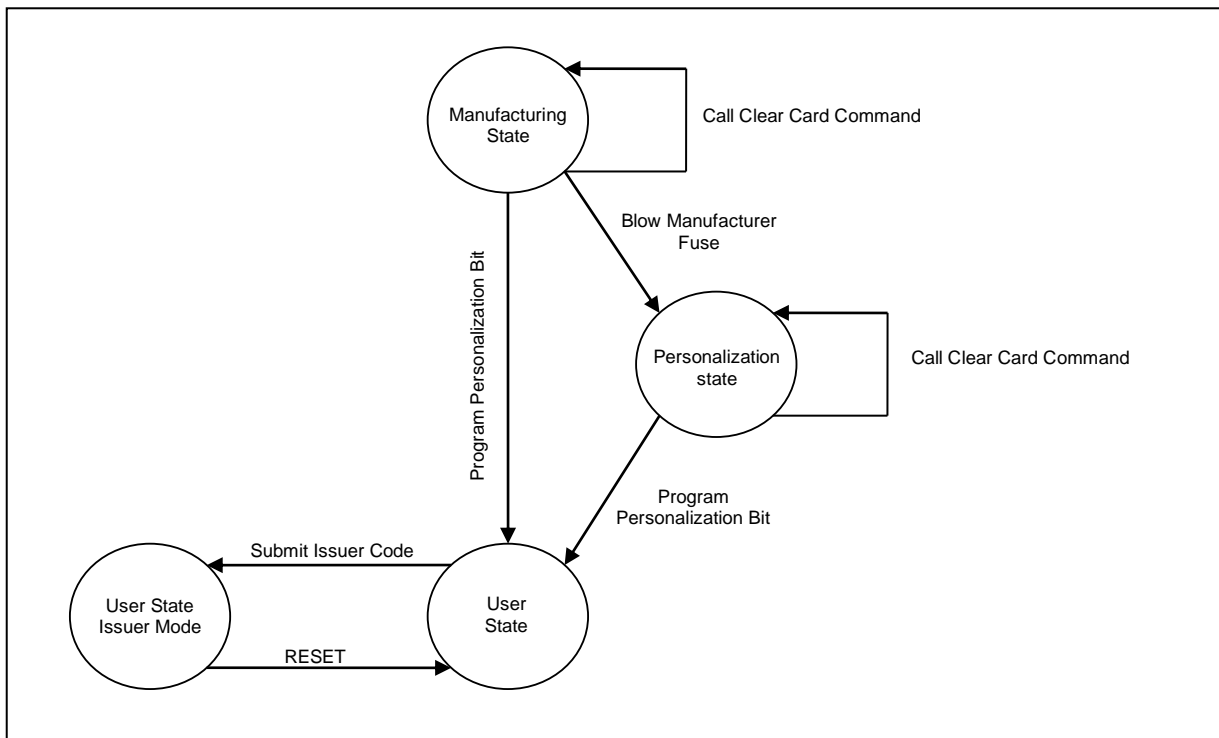
This section outlines the card level features and management functions.

#### 3.1. Card Life Cycle States

During the whole life cycle of the chip-card, three phases and two different operating modes can be distinguished:

- Manufacturing State
- Personalization State
- User State
- User State - Issuer Mode

The card is at any moment in one of these four states. The following diagram shows the possible transitions between the four states:



**Figure 1: Card Life Cycle Diagram**

The actual card life cycle state is determined by the card operating system immediately after a reset. The life cycle state does not change during the operation of the card. CLEAR CARD command can be issued in the personalization state and manufacture state to clear the card of all data except the manufacturer data and IC code. However, this command cannot be used to User State.

#### 3.1.1. Manufacturing State

The **Manufacturing State** is effective from the moment of chip manufacturing until an associated fuse (i.e., a certain bit in the EEPROM), the *Manufacturer Fuse*, has been programmed.

The IC is presented to the card in plain, without encryption.

All commands are available in manufacturer state. In addition, the *Manufacturer File* (FF01h) can only be written in this state.



The manufacturer file contains two records, 8 bytes each, associated to the manufacturing state. In this file, it contains the *Manufacturer Fuse*. After programming the Manufacturer Fuse, the card enters the personalization state and the manufacturer file is on read-only. Data unique to each card and common card data can be programmed, such as, card manufacturer identification, card serial number, etc. The card does not interpret the data.

In this state, the card's data and keys can be erased by calling the CLEAR CARD command. This command will physically erase the EEPROM memory except for the IC code and manufacturer file.

Once the manufacturer fuse has been blown the manufacturing state will be terminated, thus there is no possibility of resetting the card back into the manufacturing state.

### **3.1.2. Personalization State**

**Personalization State** is effective from the moment of termination of the manufacturing state until an associated bit in the EEPROM, the so-called *Personalization Bit*, has been programmed.

In this state, the card's data and keys can be erased by calling the CLEAR CARD command. This command will physically erase the EEPROM memory except for the IC code and manufacturer file. Re-personalization of the card is possible.

In the Personalization State, any write access to Internal Data Files, as well as the read access to the Security File is only possible after the presentation of the correct IC code. The card manufacturer writes the IC code in the Manufacturing State.

The IC is presented to the card in plain, without encryption. The Authentication Process should not be executed prior to programming the correct keys in the Personalization State.

Once the Personalization Bit has been programmed and the Personalization State has thus been terminated, there is no possibility of resetting the card back into the personalization state.

### **3.1.3. User State**

**User State** designates the normal operating mode of the card. There are two types of User States – the User State and the User State - Issuer Mode. The User State is effective from the moment of termination of the personalization state. Most card holder operation should occur in this state.

A submission of the Issuer Code changes the operation mode to Issuer Mode. This privileged mode allows access to certain memory areas, which are otherwise not accessible.

## **3.2. Answer To Reset (ATR)**

After hardware reset (e.g., power up), the card transmits an Answer To Reset (ATR) in compliance with the standard ISO 7816, Part 3. ACOS3X supports the protocol type T=0. The protocol type selection function is not implemented.

### **3.2.1. Customizing the ATR**

There are two ways of customizing ACOS3X's ATR. The first way is to add personalization information to the Personalization File FF 02h byte 1 to byte 8. This will be fetched upon power-up in the default ATR's historical bytes stated in the previous section.

In ACOS3X, additional capabilities allow for the modification to the card's transmission speed or completely customizing the historical bytes based on the application developer's preference. These ATR modifications are achieved by writing to the internal file FF 07h after submission of the IC code.

#### **3.2.1.1. Customized ATR for Microsoft Windows® Usage**

For Windows® 7 and above operating systems: Windows automatically attempts to download the smart card's minidriver whenever a smart card is inserted into the smart card reader. Since ACOS3X is not intended to conform to Windows default usage, such smart card minidriver is not necessary. However, if the ACOS3X is inserted into a Windows system, Windows may search online for the driver and may give a warning that the "device driver was not successfully installed" for the smart card. There are two ways to solve this issue:



1. Disable smart card plug and play and certificate propagation in Windows.
2. Change the ATR so Windows will recognize the ACOS3 smart card to use ACS's Unified Null Driver.

For the first solution, please follow instructions in this Microsoft® support link to disable smart card plug and play. This may have to be done for every computer that will be used in this system. <http://support.microsoft.com/kb/976832>.

For the second solution, ACS has developed a Unified Null driver for the ACOS line of smart cards. The Unified Null driver will satisfy the Windows requirement to have a minidriver for the card, hence the warning from Windows every time the card is inserted will be removed. The Unified Null Driver can be downloaded automatically from Windows Update if Automatic Updates are turned ON. In order for Windows to recognize the ACOS3X smart card to use the Unified Null driver, the ATR must be customized. In the case of the ACOS3X, the ATR should be:

3B BE XX 00 00 41 43 53 5F 41 43 4F 53 33 5F 4E 44 90 00h.

The XX is the value of TA1. The TA1 value can be set to the baud rate that the smart card reader used can support. For ACS ACR38 for example, the TA1 value can be set to TA1=96h.

To change the ATR, perform the following steps:

1. Submit IC code (assuming default IC code): 80 20 07 00 08 41 43 4F 53 54 45 53 54h.
2. Select ATR file (FF 07): 80 A4 00 00 02 FF 07h.
3. Write the customized ATR for ACOS3 on the ATR's Historical Bytes: 80 D2 00 00 10 96 0E 41 43 53 5F 41 43 4F 53 33 5F 4E 44 90 00h.

Reset the card. The ATR is now 3B BE 96 00 00 41 43 53 5F 41 43 4F 53 33 5F 4E 44 90 00h.





## 4.0. EEPROM Memory Management

The user EEPROM memory area provided by the card chip is fully usable for user data storage. There is an additional EEPROM area that stores internal card configuration data.

- The User Data Memory stores the data of the card under the control of the application.
- The internal card configuration data is used by the card operating system to manage card functionalities.

### 4.1. Data Files

Access to both the Internal Data Memory area and the User Data Memory area is possible within the scopes of data files and data records. Data files in the Internal Data Memory are referred to as *Internal Data Files*. Data files in the User Data Memory are called *User Data Files*.

Data files are the smallest entity to which individual security attributes can be assigned to control the read and write access to the data stored in the EEPROM.

Data files are of either record type or transparent type.

### 4.2. Data File Access Control

Two security attributes are assigned to each Data File: the Read Security Attribute and the Write Security Attribute. Security attributes define the security conditions that must be fulfilled to allow the respective operation:

- The **Read Security Attribute** controls the read access to the data in a file through the READ RECORD/BINARY command. If the security condition specified in the Read Security Attribute is not fulfilled, the card will reject a READ command to that file.
- The **Write Security Attribute** controls the write access to the data in a file through the WRITE RECORD/BINARY command. If the security condition specified in the Write Security Attribute is not fulfilled, the card will reject a WRITE command to that file.

The Read Security Attribute and the Write Security Attribute for each data file specify which Application Code, if any, were submitted correctly to the card to allow the respective operation, and whether the Issuer Code and/or the PIN code must have been submitted.

A logical OR function applies to the specified Application Codes, AC x, i.e., if more than one Application Code is specified in a security attribute, the security condition is fulfilled if any one of the specified Application Codes has been correctly submitted.

A logical AND function applies to the PIN and the IC code, i.e., if PIN and/or IC are specified in a security attribute, the PIN and/or IC code(s) must have been submitted in addition to the specified Application Codes(s).

### 4.3. Internal Data Files

With exception of the Account Data Structure, which has associated a special set of commands, the memory areas of the Internal Data Memory are processed as data files.

The attributes of the Internal Data Files are defined in the card operating system and cannot be changed. However, the security attributes depend on the card life cycle state.

### 4.4. User Data Files

User Data Files are allocated in the Personalization State of the card life cycle. There are two types of User Data Files, Record and Binary files. Record files are specified by number of records and a fixed record length. Binary files are specified by a file size and accessed via offsetting into the file.

The data stored in a User Data File can be read through the READ RECORD/BINARY command and updated through the WRITE RECORD/BINARY command when the security conditions associated to the data file are fulfilled.



User Data Files are defined by writing the corresponding File Definition Blocks in the records of the User File Management File during the Personalization State. It is not possible to change the number of records of a file once any of the User Data Files has been used. User will be able to access these data as long as it's within the capacity of the card.

#### **4.5. Data File Access**

The process of data file access is identical for Internal Data Files and for User Data Files.

#### **4.6. Account Data Structure**

The Account Data Structure - *Account*, for short - is dedicated for the use in applications in which a numeric value representing some 'amount' must be securely processed. The Account is stored in the Account File.

In the User State of the card life cycle, the data in the Account cannot be manipulated by WRITE instructions like the data in User Data Files. A set of dedicated instructions is available for the processing of the Account, i.e. for adding value to and subtracting value from the balance in the Account and for reading the current balance.

Different access conditions can be specified for adding to, subtracting from and reading the Account.

Critical Account operations, for example, CREDIT, are carried out under strict security control conditions.



## 5.0. Security Features

The following security mechanisms are provided by the ACOS3X card operating system:

- DES/3DES and MAC Calculation
- Mutual Authentication and Session Key Generation
- Secret Codes
- Secure Messaging
- Secure Account Transaction Processing
- Anti-tearing Mechanism

DES refers to the DEA algorithm for data encryption and decryption as specified in the standard ANSI X3.93. MAC refers to the algorithm for the generation of cryptographic checksums (DEA in Cipher Block Chaining mode) as specified in the standard ANSI X3.93.

**Mutual Authentication** is a process in which both the card and the Card Accepting Device verify that the respective counterpart is genuine. The Session Key is a result of the successful execution of the Mutual Authentication. It is used for data encryption and decryption during a 'session'. A session is defined as the time between the successful execution of a Mutual Authentication procedure and a reset of the card or the execution of another START SESSION command.

Secret Codes and the PIN code are used to selectively enable access to data stored in the card and to features and functions provided by the card, for example, the READ and WRITE commands.

Secure messaging ensures data transmitted between the card and terminal/server is secured and not susceptible to eavesdropping, replay attack and unauthorized modifications. This is achieved by signing the command and response with a MAC and encrypting command and response data.

The Account Transaction Processing provides mechanism for the secure and auditable manipulation of data in the Account Data Structure, in particular, the balance value.

### 5.1. DES/3DES/AES and MAC Calculation

All keys used in DES/3DES/AES and MAC calculation are 8/16/24 bytes long depending on 3DES/AES selection in *Option Register* and AES/3KEY selection in *Extended Algorithm Register*. The least significant bit of each byte of the key is not used in the calculation and is not interpreted by the card operating system.

### 5.2. Mutual Authentication and Session Key Generation

The Mutual Authentication is based on the exchange and mutual verification of secret keys between the Card and the Card Accepting Device. The key exchange is performed in a secure way by use of random numbers and DES/3DES data encryption.

The session key is the final result of the Mutual Authentication process and it is based on the random numbers of both card and terminal.

The successful completion of the Mutual Authentication is recorded in the card. The resulting Session Key  $K_s$  is used for all data encryption and decryption during the same session.

The card maintains an error counter  $CNT K_T$  to count and limit the number of consecutive unsuccessful executions of the AUTHENTICATE command.

The Card Random Number  $RND_C$  is derived in a complex non-predictable mathematical process from the Random Number Seed stored in the Security File. The Random Number Seed is internally updated by the Operating System after each START TRANSACTION command.

### 5.3. Secret Codes

Secret codes stored in the card are used to restrict the access to data stored in user data files and to certain commands provided by the card. Secret codes must be presented to the card to be able to



read data from or write data to user data files and to execute certain privileged card commands.

## 5.4. Secure Messaging

ACOS3X supports Secure Messaging (SM) for data files. Secure messaging ensures data transmitted between the card and terminal/server is secured and not susceptible to eavesdropping, replay attack and unauthorized modifications. User data file can be specified that secure messaging is required for READ/WRITE RECORD/BINARY commands. Almost all the other commands can also use secure messaging initiated by the terminal.

The SM employed in ACOS3X both encrypts and signs the data transmitting into and out of the card.

## 5.5. Account Transaction Processing

Associated to the Account are four keys:

- The Credit Key  $K_{CR}$
- The Debit Key  $K_D$
- The Certify Key  $K_{CF}$
- The Revoke Debit Key  $K_{RD}$

The keys are stored in the Account Security File.

The keys are used in the calculation and verification of MAC cryptographic checksums on commands and data exchanged between the card and the Card Accepting Device in the Account processing.

## 5.6. Anti-tearing Mechanism

Anti-tearing mechanism help protects card data and security in the event that the card is suddenly powered down or pulled out during a card operation.

When writing user data into the card, ACOS3X's anti-tearing mechanism ensures the operation is performed atomically. That is, data is either completely written or the target writing area is left at its previous state before the write operation. The account data file is protected similarly when performing CREDIT/DEBIT/REVOKE DEBIT commands.



## **6.0. Life Support Application**

These products are not designed for use in life support appliances, devices, or systems where malfunction of these products can reasonably be expected to result in personal injury. ACS customers using or selling these products for use in such applications do so on their own risk and agree to fully indemnify ACS for any damages resulting from such improper use or sale.



## 7.0. Contact Information

For additional information please visit <http://www.acs.com.hk>.

For sales inquiry please send e-mail to [info@acs.com.hk](mailto:info@acs.com.hk).